

# EMI

The diagram illustrates a 10-stage digital-to-analog converter (DAC). A large red arrow labeled "D → 10" points from left to right, indicating the conversion process. Below the arrow, the input binary value is shown as a 10-bit register: **D** = **AC 0 0 T4 T5**. Each bit is represented by a square box containing two circles. The first four bits (AC, 0, 0, T4) are grouped together in a larger box, and the next three bits (T4, T5) are also grouped together. The final three bits (T4, T5) are shown separately below the main group. To the right of the DAC stages, there is a small circuit labeled **UUT1 +** and **UUT1 -**, which likely represents the operational amplifiers used in the DAC's output stage.

