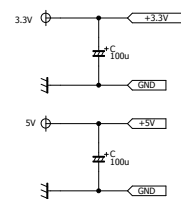


DIR_SRC Ver1.00 回路図1/2
EMISUKE AUDIO 2006/05



CPUモード

J1	表示デバイス選択	OPEN	LED
J2-13		GND	VFD
		OPEN	

OUTPUT FORMAT				
	Left Justified	I2S	TDM	Right Justified
OFMT0 J8	GND	OPEN	GND	OPEN
OFMT1 J9	GND	GND	OPEN	OPEN

OUTPUT DATA BIT				
	24bit	20bit	18bit	16bit
OWL0 J10	GND	OPEN	GND	OPEN
OWL1 J11	GND	GND	OPEN	OPEN

ハードウェアモード

出力サンプリングレート選択						
	48K	96K	192K	88.2K	44.1K	96K
SR J5	GND	GND	OPEN	OPEN	OPEN	OPEN
F1 J6	GND	GND	GND	OPEN	OPEN	GND
1/2 J7	GND	OPEN	OPEN	OPEN	GND	GND
FS	12.288M	12.288M	24.576M	11.2896M	11.2896M	24.576M
12FS	6.144M	12.288M	24.576M	11.2896M	5.6448M	12.288M

OUTPUT FORMAT				
	Left Justified	I2S	TDM	Right Justified
OFMT0 J8	GND	OPEN	GND	OPEN
OFMT1 J9	GND	GND	OPEN	OPEN

OUTPUT DATA BIT				
	24bit	20bit	18bit	16bit
OWL0 J10	GND	OPEN	GND	OPEN
OWL1 J11	GND	GND	OPEN	OPEN

SRCバイパス		POW LED	
J12	OFF	J13	ON
	OPEN		OPEN